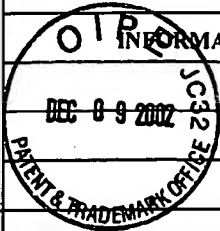


U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-2928-2		
				Application No.: 09/986,231		
				Applicant(s): William Bush et al.		
(Use several sheets if necessary)				Filing Date: October 22, 2001		
				Group Art Unit: 2124		
				Date Submitted: December 3, 2002		
<b>U.S. Patent Documents</b>						
*Examiner Initial		Document Number	Date	Name		
	AA					
	AB					
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<b>Foreign Patent Documents</b>						
					Translation	
		Document	Date	Country	Yes	No
	AL					
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<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
M	AQ	Jones & Lins, "Garbage Collection: Algorithms for Automatic Dynamic Memory Management," Wiley (1996), pp. 1-41.				
M	AR	Appel, "Modern Compiler Implementation in C: Basic Techniques", Cambridge University Press (1998), pp. 125-149 and pp. 291-297.				
M	AS	Weaver and Germond, "The SPARC Architecture Manual Version 9, Prentice-Hall, Inc. (1994), pp. 237-238.				
M	AT	U. S. App. No. 09/229,272, filed January 13, 1999, titled "Thread Suspension System and Method," naming William Bush and Mario Wolczko inventors.				
Examiner <i>M. Hutton</i>		Date Considered 12.9.04				
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s): William Bush et al.
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NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
MA	AA	Sudharsanan, Subramania, "MAJC-5200: A High Performance Microprocessor for Multimedia Computing," IPDPS 2000 Workshop on Parallel and Distributed Computing in Image Processing, Video Processing, and Multimedia (PDIVM 2000), <i>Lecture Notes in Computer Science</i> , 1800 Springer 2000, ISBN 3-540-67442-X, pp. 163-170, May 1-5, 2000, Cancun, Mexico, 4 pages [online], Retrieved from the Internet, printed October 29, 2002 <URL: <a href="http://www.sun.com/processors/whitepapers/majctutorial.pdf">http://www.sun.com/processors/whitepapers/majctutorial.pdf</a> >.
MA	AB	"MAJC™ Architecture Tutorial" Sun Microsystems, Inc., White Paper, September 1999, 18 pages [online], Retrieved from the Internet, printed October 29, 2002 <URL: <a href="http://www.sun.com/processors/whitepapers/ipdps_Sudhafinal.pdf">http://www.sun.com/processors/whitepapers/ipdps_Sudhafinal.pdf</a> >.
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